

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

Date: November 28, 2006

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Atty.	M#	Client Ref.
Bollman	20-143	20-143
Applicant: BALAY et al.		
Appln. No.: 09/966,095		
Filing Date: October 1, 2001		
Examiner: DANG, Khanh		
Group Art Unit: 2111		

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140-28 NOV 28 2006
SEARCHED INDEXED
SERIALIZED FILED
USPTO-ART UNIT 2111

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	SubClass	Filing Date (if appropriate)
AR						
BR						
CR						
DR						
ER						
FR						
GR						
HR						
IR						
JR						
KR						
LR						
MR						
NR						

FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Inventor Name	Class	SubClas s	English		Translation	
							Enclosed	No	Enclosed	No
OR										
PR										
QR										
RR										
SR										
TR										
UR										
VR										

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

WR	LATTICE SEMICONDUCTR CORPORATION, "ORCA® ORT-4622 Field Programmable System Chip (FSPC) Four-Channel x 622 Mbit/s Backplane Transceiver", Preliminary Data Sheet, October 2003pp. 1.79			
XR				
YR				
ZR				
AAR				
BBR				

Examiner

Date Considered:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.